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7590 02/03/2004

MATTINGLY, STANGER & MALUR, P.C.
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/084,435	02/28/2002	Naoki Katoh		8143

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, STORAGE MEDIUM ON WHICH CELL LIBRARY IS STORED AND DESIGNING METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
KIK, PHALLAKA	2825	716-017000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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Shrinath Malur, Reg. No. 34,663 4/30/04

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01 FC:1501
02 FC:1504

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